

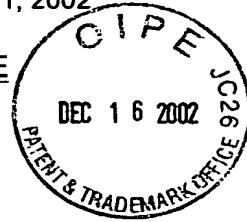
Docket No.: MAS-FIN-116

2811
1D/ELECTION
1-6-03
Mulloni

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below:

By: Markus Nolff

Date: December 11, 2002



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : Johann Winderl et al.
Applic. No. : 10/079,114
Filed : February 20, 2002
Title : Electronic Component with a Semiconductor Chip and Method
of Producing an Electronic Component
Examiner : Gene M. Munson - Art Unit: 2811

R E S P O N S E

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Responsive to the Office Action dated , the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, 1-17, for prosecution at this time.

Applicants request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-17 is solicited.

Respectfully submitted,

Markus Nolff
For Applicants

MARKUS NOLFF
REG. NO. 37,006

Date: December 11, 2002

Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101
/bmb

RECEIVED
DEC 17 2002
TECHNOLOGY CENTER 2800